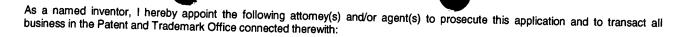
As a below named inventor, I hereby declare that:

## CLARATION FOR UTILITY OF SIGN PATENT APPLICATION (37 CFR 1.63)

Attorney Docket No.: ORCH 0183 PUS

designating the not disclosed in §112, I acknowlavailable between Prior U.S. Applic PCT Parent I	en the filing cation(s) OF Number:	(Day/Mor	nth/Year Filed) of any United States prov	(Patented, pendi	ing, abandoned)
designating the not disclosed in §112, I acknowl available between Prior U.S. Applic PCT Parent I	en the filing cation(s) OF Number:				
designating the not disclosed in §112, I acknowlavailable between Prior U.S. Applic	en the filing cation(s) OF	, ,,	and the manerial of		date of this application:
designating the not disclosed in §112, I acknow			and and the transfer of	To the state of th	date of this application:
l barabu alaim i	United State the prior U	es of America, listed I Inited States or PCT luty to disclose inform	below and, insofar as the International Application	e subject matter of each in the manner provided to patentability as define	f any PCT International Applicat h of the claims of this application d by the first paragraph of 35 U ed in 37 CFR §1.56 which beca
(Number)		(Country)	(Day/Month/Year F	,	f any PCT International Applicat
				Priority Claimed No	Copy AttachedYesNo
Prior Foreign Ap	plication(s)				Certified
certificate, or 3 America, listed	65(a) of an below and	y PCT International A have also identified	Application which design	ated at least one count lication for patent or in	oplication(s) for patent or inventor try other than the United States eventor's certificate, or of any P ed:
I acknowledge ti	ne duty to d	sclose information wh	ich is material to patental	bility as defined in 37 CF	FR § 1.56.
		reviewed and under nt specifically referred		he above identified spe	ecification, including the claims,
			Application Number		
(check one)	$\square$	is attached hereto was filed on	; as U.S. Application (if applicable)	on Serial No	; and was amended on
the specification		METHOD AND SYSTE	EM FOR DETECTION O	F AN ANALYTICAL CHI	P PLATE
	·	·	ch is claimed and for whi		
	t halow) of t				nal, first and joint inventor (if plu
names are listed		first and sole invento	or (if only one name is li	:-td b-al	

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 USC 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of sole or first inventor: <u>Valery Bogdanov</u>	
Inventor's signature V. Bogdawov	Date_10/27/2001
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